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ADRIENNE CHOCHOLAK
(SENDER'S PRINTED NAME)


(SIGNATURE)

Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231

Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor In-Hak PARK, et al.:
For: SEMICONDUCTOR WAFER POSITION DETECTING SYSTEM
SEMICONDUCTOR DEVICE FABRICATING FACILITY OF USING
THE SAME AND WAFER POSITION DETECTING METHOD
THEREOF

Applicant requests FIG. 3 to be published with the application.

Enclosures:

- ☒ Specification (pages 1-11); claims (pages 12-18); abstract (page 16)
- ☒ FOUR sheet(s) of FORMAL drawings
- ☒ Declaration or Combined Declaration and Power of Attorney
- ☒ Newly executed (original or copy)
- ☒ Assignment with cover sheet ✓
Assignee Name and Address: Samsung Electronics Co., Ltd.
Suwon-city, Kyungki-do, Korea
- ☒ Certified copy of priority document
- ☒ Return Postcard

CLAIMS AS FILED				
For	Number Filed	Number Extra	Rate	Basic Fee \$740
Total Claims	18-20	0	x \$ 18 =	0
Independent Claims	3-3	0	x \$ 84 =	0
TOTAL FILING FEE				\$740.00

- ☒ PTO Form 2038 authorizing credit card payment for the above-listed fees is enclosed.
☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A duplicate copy of this sheet is enclosed.



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Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Alan T. McCollom
Reg. No. 28,881

MARGER JOHNSON & McCOLLOM, P.C.
1030 SW Morrison Street
Portland, OR 97205
503-222-3613